

U.S. Patent Application Serial No. 10/775,216
Amendment filed March 28, 2007
Reply to OA dated November 30, 2006

REMARKS

Claims 1 - 28 have been canceled without prejudice or disclaimer.

Claims 29 - 38 have been added in order to more particularly point out, and distinctly claim the subject matter to which the applicants regard as their invention. It is believed that this Amendment is fully responsive to the Office Action dated November 30, 2006.

As to the merits of this case, the Examiner has withdrawn his previous anticipation rejection under 35 USC §102(b) based on Tominaga (U.S. Patent No. 5, 237,208. However, the Examiner now relies on a new reference (namely, Otani, U.S. Patent No. 7,045,915) in setting forth the following rejections:

- (1) claims 13 - 18, 20 - 22 and 24- 28 stand rejected under 35 USC §103(a) based on Tominaga in view of Otani; and
- (2) claims 19 and 23 stand rejected under 35 USC §103(a) based on Tominaga in view of Otani, and further in view of Luo (U.S. Patent Publication No. 2005/0073783).

The applicants respectfully request reconsideration of these rejections.

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The applicants' instant claimed invention provides a downsizeable multiple output power source apparatus with effective and optimized voltage outputting by:

- (1) making possible simultaneous shut down of any plural power source circuits only by connecting these any plural power source circuits of a plurality of the power source circuits by means of the fault terminal HLT; and
- (2) making possible simultaneous oscillation of the control circuit of a master power source circuit and the control circuits of slave power source circuits only by selecting any power source circuit of a plurality of the power source circuits as a master power source circuit and any one of plural power source circuits as slave power source circuits, and connecting the master power source circuit to the slave power source circuits by means of CLK & HLT terminal.

The primary reference of Tominaga, on the other hand, describes that, in a configuration in which a plurality of triport uninterruptable power source devices (UPS) are connected in parallel, (1) load to each UPS is in proportion to the capacity thereof; (2) reducing of circulating current and synchronous operations between inverters are made possible; and (3) cutting off a faulty UPS without any trouble.

The secondary reference of Otani describes a power supply device outputting mutually different plural voltages.

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The other secondary reference of Luo describes a structural arrangement in which abnormality signal and synchronous signal are inputted and outputted through a single terminal.

However, Tominaga, Otani and Luo, singly or in combination, do not disclose or suggest the above-discussed claimed invention, as now recited in the added claims filed herewith. Accordingly, a person of ordinary skill in the art would not have found the applicants' claimed invention obvious under 35 USC §103(a) based on Tominaga, Otani and Luo, singly or in combination.

In view of the above, the withdrawal of the outstanding obviousness rejections under 35 USC §103(a) based on Tominaga in view of Otani, and under 35 USC §103(a) based on Tominaga in view of Otani, and further in view of Luo is in order, and is therefore respectfully solicited.

In view of the aforementioned amendments and accompanying remarks, claims, as amended, are in condition for allowance, which action, at an early date, is requested.

If, for any reason, it is felt that this application is not now in condition for allowance, the Examiner is requested to contact the applicants' undersigned attorney at the telephone number indicated below to arrange for an interview to expedite the disposition of this case.

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In the event that this paper is not timely filed, the applicants respectfully petition for an appropriate extension of time. Please charge any fees for such an extension of time and any other fees which may be due with respect to this paper to Deposit Account No. 01-2340.

Respectfully submitted,

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